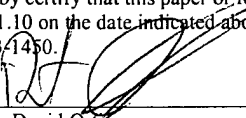


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	KIDA et al.	Examiner:	Kiley Shawn Stoner
Serial No.:	10/616,194	Group Art Unit:	1725
Filed:	July 9, 2003	Docket:	10873.1263US01
Confirmation No.:	9668	Notice of Allow. Date:	December 6, 2004
Due Date:	March 7, 2005		
Title:	METHOD FOR MANUFACTURING PRINTED CIRCUIT BOARD		

**CERTIFICATE UNDER 37 CFR 1.10:**  
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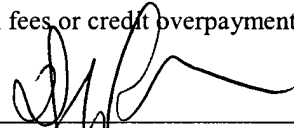
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